

part of # 11



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
ELIYAHOU HARARI et al.)
Serial No.: 08/771,708)
Filed: December 20, 1996)
For: FLASH EEPROM SYSTEM)

Group Art Unit: 2785

San Francisco, California

Assistant Commissioner of Patents
Washington, D.C. 20231

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231 on 2-12-98

Carol Mendel

Carol Mendel Feb. 12, 1998.
Signature Date

SUPPLEMENTAL DISCLOSURE STATEMENT

Sir:

The following Form 1449 and copies of each cited document are being filed herewith as a Supplemental Disclosure Statement. Consideration of each of these documents by the Patent Examiner, and the making of each of them of record in the file of this application, are respectfully requested.

Respectfully submitted,

Dated: Feb. 12, 1998.

Gerald P. Parsons
Gerald P. Parsons, Reg. No. 24,486
MAJESTIC, PARSONS, SIEBERT & HSUE PC
Four Embarcadero Center, Suite 1100
San Francisco, CA 94111-4106
Telephone: (415) 248-5500
Facsimile: (415) 362-5418

Atty. Docket: HARI.006USE

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INFORMATION DISCLOSURE STATEMENT

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Respectfully submitted,

Dated: Jan. 7, 1997.

Gerald P. Parsons
Gerald P. Parsons, Reg. No. 24,485
MAJESTIC, PARSONS, SIEBERT & HSUE PC
Four Embarcadero Center, Suite 1100
San Francisco, CA 94111-4106
Telephone: (415) 248-5500
Facsimile: (415) 362-5418

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**SCHEDULE A
ISSUED PATENTS**

PATENT#	ISSUED	INVENTORS
5,070,032	12/03/91	Jack H. Yuan Eliyahou Harari
5,163,021	11/10/92	Sanjay Mehrotra Eliyahou Harari Winston Lee
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5,270,979	12/14/93	Eliyahou Harari Daniel C. Guterman Sanjay Mehrotra Stephen J. Gross
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5,315,541	05/24/94	Eliyahou Harari Sanjay Mehrotra
5,343,063	08/30/94	Jack H. Yuan Gheorghe Samachisa Daniel C. Guterman Eliyahou Harari
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FORM PT01449
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ATTY. DOCKET NO.

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HARI.006USE

08/771,708

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT

Eliyahou Harari et al.

FILED

December 20, 1996

GROUP

2785

U. S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
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	A30 4 2 7 2 8 3 0	1981	Moench			
	A31 4 2 8 7 5 7 0	1981	Stark			
	A32 4 5 8 6 1 6 3	1986	Koike			
	A33 4 6 5 3 0 2 3	1987	Suzuki et al.			
	A34 4 8 4 7 8 0 8	1989	Kobatake			

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DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANS.? (YES/NO)

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

- C21 Stark, M., "Two Bits Per Cell Rom," IEEE Catalog No. 81-CH1626-1, Library of Congress No. 80-85186, Order No. 341, Spring Comp Con 1981, Feb. 23-26, Twenty-Second Computer Society International Conference, San Francisco, CA
- C22 Furuyama et al., "An Experimental 2-Bit/Cell Storage Dram For Macro Cell Or Memory-On-Logic Application," pp. 4.4.1 - 4.4.4, IEEE, (1988)
- C23 Horiguchi et al., "An Experimental Large-Capacity Semiconductor File Memory Using 16 Levels Cell Storage," IEEE Journal Of Solid-State Circuits, Vol. 23, No. 1, February 1988

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.